As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled ASSEMBLIES INCLUDING STACKED SEMICONDUCTOR DEVICES SEPARATED BY DISCRETE CONDUCTIVE ELEMENTS THEREBETWEEN, PACKAGES INCLUDING THE ASSEMBLIES, AND METHODS, the specification of which (check one):

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	was filed on was filed on		application serial no onal application no.		nended on anded under PCT Article 19 on		
	was med on	as FCT internati	onar application no	and was ame	anded under FC1 Article 19 on	 •	
		viewed and underst	and the contents of the abo	ve-identified specific	cation, including the claims, as am	ended by any an	nendment
referred	to above.						
			Patent and Trademark Offi is defined in Title 37, Cod		nown to me to be material to the pations § 1.56.	stentability of the	e subject
certifica attached any PC	ite or § 365(a) of any P I continuation page and	CT international application of the control of the	plication(s) designating at l below and on any attache	east one country oth d continuation page	365(b) of any foreign application(s her than the United States of Ameri any foreign application for patent of America having a filing date be	ca listed below a or inventor's cer	and on any tificate or
Prior fo	reign/PCT application((s):				Priority Clain	ned
	(number)		(cour	ntry)	(day/month/year filed)	Yes	No
Q Q	(number)		(cour	ntry)	(day/month/year filed)	Yes	No
ility to	disclose to the U.S. Pa	tent and Trademark	Office all information kno	wn to me to be mate	graph of Title 35, United States Co crial to patentability as defined in T	itle 37, Code of	Federal
9					the national or PCT international fi		application:
<u> </u>	(application serial n		(filing date)		(status - pending, patented of		application:
01 * Cl		o.))		or abandoned)	application:
	(application serial n	0.)	(filing date)		(status - pending, patented of	or abandoned)	application:
	(application serial n	o.) o.) under Title 35, Unite	(filing date)		(status - pending, patented of	or abandoned)	application:
O O O U I I I her	(application serial n (application serial n reby claim the benefit to (provisional applications)	o.) o.) under Title 35, Unite	(filing date) (filing date)	f any United States p	(status - pending, patented of	or abandoned) or abandoned) ow:	

both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon. Sudem Date 8/16/2001

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or

Full name of sole inventor: James M. Derderian

Inventor's signature

Residence: Boise, Idaho Citizenship: U.S.A.

Post Office Address: 4048 Iriondo Way, Boise 10 83706



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serial No.:

James M. Derderian Not yet assigned Examiner:

Unknown Unknown

Filed:

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Group Art Unit: Attorney Docket No.:

4832US (01-0104)

Title: ASSEMBLIES INCLUDING STACKED SEMICONDUCTOR DEVICES SEPARATED BY DISCRETE CONDUCTIVE ELEMENTS THEREBETWEEN, PACKAGES INCLUDING THE ASSEMBLIES, AND METHODS

POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., assignee of the entire right, title and interest by assignment from the inventor(s) in the above-identified application, hereby appoints the following attorneys and agents:

David V. Trask, Reg. No. 22,012 Joseph A. Walkowski, Reg. No. 28,765 Allen C. Turner, Reg. No. 33,041 Brick G. Power, Reg. No. 38,581 Krista Weber Powell, Reg. No. 47,867 Shawn G. Hansen, Reg. No. 42,627 Katherine A. Hamer, Reg. No. 47,628

William S. Britt, Reg. No. 20,969 James R. Duzan, Reg. No. 28,393 Edgar R. Cataxinos, Reg. No. 39,931 Paul C. Oestreich, Reg. No. 44,983 Jarett K. Abramson, Reg. No. 47,376 Bretton L. Crockett, Reg. No. 44,632 Michael L. Lynch, Reg. No. 30,871 Laurence B. Bond, Reg. No. 30,549 H. Dickson Burton, Reg. No. P-48,396 Kent S. Burningham, Reg. No. 30,453 Devin R. Jensen, Reg. No. 44,805 David L. Stott, Reg. No. 43,937 Bradley B. Jensen, Reg. No. 46,801 Charles B. Brantley II, Reg. No. 38,086

as its attorneys with full power of substitution to prosecute this application and all applications claiming filing date priority therefrom and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee hereby elects, pursuant to 37 C.F.R. § 3.71, to conduct the prosecution of the above-identified patent application to the exclusion of the inventor(s).

A chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee is shown:

[] In an assignment recorded in the U.S. Patent and Trademark Office at Reel, Frame.

[x] In an assignment filed herewith for recordation, a true copy of which is attached hereto.

The undersigned has reviewed the above-identified assignment and, to the best of his knowledge and belief, title is in the above-identified assignee.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the above-identified assignee, and to take the action set forth herein on its behalf.

Please direct all communications regarding the above-identified application to:

Brick G. Power, TRASKBRITT, PC P.O. Box 2550 Salt Lake City, UT 84110 Tele: (801) 532-1922 Fax: (801) 531-9168

Respectfully Submitted,

MICRON TECHNOLOGY, INC.

Date: 8 -16-01

Michael L. Lynch, Esq. Reg. No. 30,871

Chief Patent Counsel, MICRON TECHNOLOGY, INC.

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